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**SCULLY, SCOTT, MURPHY  
& PRESSER****Fax**

|               |   |               |                                    |
|---------------|---|---------------|------------------------------------|
| <b>To:</b>    | Examiner Ori NADAV<br>Art Unit: 2811  | <b>From:</b>  | Leslie S. Szivos                   |
| <b>Fax:</b>   | 571-273-8300  | <b>Pages:</b> | 13 pages including fax cover sheet |
| <b>Phone:</b> |   | <b>Date:</b>  | 1/12/2006                          |
| <b>Re:</b>    | U.S. Patent Appln. No. 10/763,308<br>Filed: January 23, 2004<br>Docket: FIS920020200US2 (16106A)<br>Confirmation No. 6529 | <b>CC:</b>    |                                    |

**RESPONSE TO OFFICE ACTION**

Please see the following documents:


1. Amendment Transmittal (in duplicate)
2. Authorization to charge IBM deposit account
3. Response under 37 C.F.R. § 1.111
4. Certificate of transmission by facsimile dated January 12, 2006

If you have any questions concerning this facsimile please contact Vivian Henriquez at 516-742-4343.  
Thank you.


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PAGE 2/13 \* RCVD AT 1/12/2006 1:12:59 PM [Eastern Standard Time] \* SVR:USPTO-EFXRF-6/30 \* DNIS:2738300 \* CSID:5167424366 \* DURATION (mm-ss):03:48

**JAN 12 2006**


|  |                                 |  |                        |
|--|---------------------------------|--|------------------------|
| CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)  |                                 | Docket No.<br>FIS920020200US2 (16106A) |                        |
| Applicant(s): Shreesh Narasimha, et al.  |                                 |  |                        |
| Application No.<br>10/763,308  | Filing Date<br>January 23, 2004 | Examiner<br>Ori Nadav                  | Group Art Unit<br>2811 |
| Invention: CMOS DEVICE INTEGRATION FOR LOW EXTERNAL RESISTANCE   |                                 |  |                        |
|  |                                 |  |                        |
| I hereby certify that this RESPONSE UNDER 37 C.F.R. § 1.111<br><i>(Identify type of correspondence)</i><br>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. 571-273-8300)<br>on January 12, 2006<br><i>(Date)</i> |                                 |  |                        |
| Leslie S. Szivos, Ph.D.<br><i>(Typed or Printed Name of Person Signing Certificate)</i><br><br><i>(Signature)</i>   |                                 |  |                        |
| Note: Each paper must have its own certificate of mailing.   |                                 |  |                        |

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|  |  |                              |   |   |                                 |
|--|--|------------------------------|---|---|---------------------------------|
| <b>AMENDMENT TRANSMITTAL LETTER (Large Entity)</b>   |  |                              |   | Docket No.<br><b>FIS920020200US2 (16106A)</b> |                                 |
| Applicant(s): <b>Shreesh Narasimha, et al.</b>   |  |                              |   |   |                                 |
| Application No.<br><b>10/763,308</b>   | Filing Date<br><b>January 23, 2004</b> | Examiner<br><b>Ori Nadav</b> | Customer No.<br><b>23389</b>  | Group Art Unit<br><b>2811</b>                 | Confirmation No.<br><b>6529</b> |
| Invention: <b>CMOS DEVICE INTEGRATION FOR LOW EXTERNAL RESISTANCE</b>  |  |                              |   |   |                                 |
| <b>COMMISSIONER FOR PATENTS:</b>   |  |                              |   |   |                                 |
| Transmitted herewith is an amendment in the above-identified application.<br>The fee has been calculated and is transmitted as shown below.  |  |                              |   |   |                                 |
| <b>CLAIMS AS AMENDED</b>   |  |                              |   |   |                                 |
|  | CLAIMS REMAINING<br>AFTER AMENDMENT    | HIGHEST #<br>PREV. PAID FOR  | NUMBER EXTRA<br>CLAIMS PRESENT  | RATE  | ADDITIONAL<br>FEE               |
| TOTAL CLAIMS   | 14 -                                   | 20 =                         | 0   | x \$50.00                                     | \$0.00                          |
| INDEP. CLAIMS  | 1 -                                    | 3 =                          | 0   | x \$200.00                                    | \$0.00                          |
| Multiple Dependent Claims (check if applicable) <input type="checkbox"/>   |  |                              |   |   | \$0.00                          |
| <b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT</b>   |  |                              |   |   | <b>\$0.00</b>                   |
| <input checked="" type="checkbox"/> No additional fee is required for amendment.<br><input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____<br><input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed.<br><input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account <b>09-0458/IBM</b><br><input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16.<br><input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17.<br><input type="checkbox"/> Payment by credit card. Form PTO-2038. |  |                              |   |   |                                 |
| <b>WARNING: Information on this form may become public. Credit card information should not be included on this form. Provide credit card information and authorization on PTO-2038.</b>  |  |                              |   |   |                                 |
| <br>_____<br>Signature  |  |                              | Dated: <b>January 12, 2006</b>  |   |                                 |
| <b>Leslie S. Szivos, Ph.D.</b><br><b>Registration No. 39,394</b><br><b>SCULLY, SCOTT, MURPHY &amp; PRESSER, P.C.</b><br><b>400 Garden City Plaza - Suite 300</b><br><b>Garden City, New York 11530</b>   |  |                              | <div style="border: 1px solid black; padding: 5px;">         I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____<br/>         _____<br/>         (Date)<br/>         _____<br/>         Signature of Person Mailing Correspondence<br/>         _____<br/>         Typed or Printed Name of Person Mailing Correspondence       </div> |   |                                 |
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|  |  |                              |   |                               |   |  |
|--|--|------------------------------|---|-------------------------------|---|--|
| <b>AMENDMENT TRANSMITTAL LETTER (Large Entity)</b>   |  |                              |   |                               | Docket No.<br><b>FIS920020200US2 (16106A)</b> |  |
| Applicant(s): <b>Shreesh Narasimha, et al.</b>   |  |                              |   |                               |   |  |
| Application No.<br><b>10/763,308</b>   | Filing Date<br><b>January 23, 2004</b> | Examiner<br><b>Orl Nadav</b> | Customer No.<br><b>23389</b>  | Group Art Unit<br><b>2811</b> | Confirmation No.<br><b>6529</b>               |  |
| Invention: <b>CMOS DEVICE INTEGRATION FOR LOW EXTERNAL RESISTANCE</b>  |  |                              |   |                               |   |  |
| <b>COMMISSIONER FOR PATENTS:</b>   |  |                              |   |                               |   |  |
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| TOTAL CLAIMS   | 14 -                                   | 20 =                         | 0   | x \$50.00                     | \$0.00  |  |
| INDEP. CLAIMS  | 1 -                                    | 3 =                          | 0   | x \$200.00                    | \$0.00  |  |
| Multiple Dependent Claims (check if applicable) <input type="checkbox"/>   |  |                              |   |                               | \$0.00  |  |
| <b>TOTAL ADDITIONAL FEE FOR THIS AMENDMENT</b>   |  |                              |   |                               | <b>\$0.00</b>                                 |  |
| <input checked="" type="checkbox"/> No additional fee is required for amendment.<br><input type="checkbox"/> Please charge Deposit Account No. _____ in the amount of _____<br><input type="checkbox"/> A check in the amount of _____ to cover the filing fee is enclosed.<br><input checked="" type="checkbox"/> The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account <b>09-0458/IBM</b><br><input checked="" type="checkbox"/> Any additional filing fees required under 37 C.F.R. 1.16.<br><input checked="" type="checkbox"/> Any patent application processing fees under 37 CFR 1.17.<br><input type="checkbox"/> Payment by credit card. Form PTO-2038. |  |                              |   |                               |   |  |
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| <br>_____<br>Signature  |  |                              | Dated: <b>January 12, 2006</b>  |                               |   |  |
| <b>Leslie S. Szivos, Ph.D.</b><br><b>Registration No. 39,394</b><br><b>SCULLY, SCOTT, MURPHY &amp; PRESSER, P.C.</b><br><b>400 Garden City Plaza - Suite 300</b><br><b>Garden City, New York 11530</b>   |  |                              | <div style="border: 1px solid black; padding: 5px;">         I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on _____<br/>         _____<br/>         (Date)<br/>         _____<br/>         Signature of Person Mailing Correspondence<br/>         _____<br/>         Typed or Printed Name of Person Mailing Correspondence       </div> |                               |   |  |
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant(s):** Shreesh Narasimha, et al.      **Examiner:** Ori NADAV  
**Serial No.:** 10/763,308      **Art Unit:** 2811  
**Filed:** January 23, 2004      **Docket:** FIS920020200US2 (16106A)  
**Dated:** January 12, 2006  
**For:** CMOS DEVICE INTEGRATION  
FOR LOW EXTERNAL RESISTANCE

**Confirmation No.:** 6529

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT UNDER 37 C.F.R. § 1.111**

Sir:

In response to the Office Action dated October 12, 2005, Applicants submit the following amendments and remarks for entry of record in the above-identified patent application:

**CERTIFICATION OF FACSIMILE TRANSMISSION**

I hereby certify that this document is being filed in the United States Patent and Trademark Office on the date shown below via facsimile transmission to Mail Stop Amendment, Commissioner for Patents, PO Box 1450, Alexandria, VA 22313-1450 at United States Patent and Trademark Office facsimile transmission number (571) 273-8300.

**Dated:** January 12, 2006  
Leslie S. Szivos, Ph.D.